



Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>					Complete if Known	
					Application Number	10/798,600
					Filing Date	March 10, 2004
					First Named Inventor:	Rohit Natarajan
					Art Unit	2186
					Examiner Name	Thomas, Shane M.
					Attorney Docket Number	42P17960
Sheet	1	of		2		
U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Patent Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³	Number ⁴ Kind Code ⁵ (if known)			
	1	US-	5,903,916	05-11-1999	Pawlowski et al.	
	2	US-	6,172,893 B1	01-09-2001	Ryan	
	3	US-	6,269,433 B1	07-31-2001	Jones et al.	
	4	US-	6,427,196 B1	07-30-2002	Adiletta et al.	
	5	US-	6,532,509 B1	03-11-2003	Wolrich et al.	
	6	US-	6,560,667 B1	05-06-2003	Wolrich et al.	
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FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
	7	WO	02/33556	A2	04-25-2002	SUN MICROSYSTEMS, INC.		

Examiner Signature		Date Considered	3/19/07
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				Examiner Name	Thomas, Shane M.
				Attorney Docket Number	42P17960
Sheet	2	of	2		
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			
<i>SN</i>	8	INTEL, Intel Technology Journal, The next Generation of Intel IXP Network Processors, Volume 06, Issue 03, 8/15/2002, pp. 1-15.			
	9	MICRON, RLD RAM II Memory, Low-Latency, High-Density, High-Bandwidth DRAM, 12/12/2003, (2 pgs.).			
	10	INTEL, Intel IXP1200 Network Processor Family, Hardware Reference Manual, December 2001, pp. 1-272.			
	11	MICRON Technical Note: TN-49-02 Exploring the RLD RAM II Feature Set; 2004, pp. 1-4.			
	12	MICRON Technical Note: TN-49-05 General DDR SDRAM Functionality; 2001, pp. 1-11.			

Examiner Signature		Date Considered	3/19/07
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